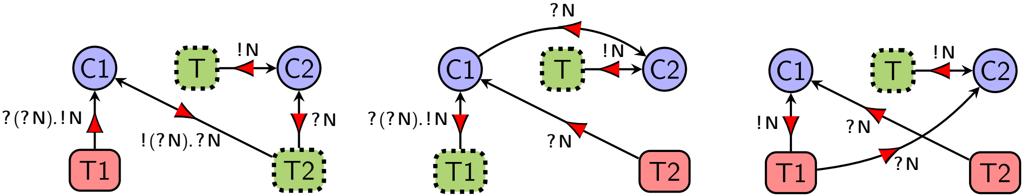
Connectivity Graphs: A Method for Proving Deadlock Freedom Based on Separation Logic

Jules Jacobs[1] Robbert Krebbers[1] Stephanie Balzer[2]



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What makes session types interesting

Message passing concurrency with first-class channels (Honda [1993])

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- Required for **type safety** (mechanized by Castro-Perez et al. [2020], Ciccone and Padovani [2020], Goto et al. [2016], Hinrichsen et al. [2021], Rouvoet et al. [2020], Thiemann [2019], ...)

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- But also guarantees **deadlock freedom**, **global progress** (well-known property, but not yet mechanized for first-class channels, i.e. dynamically allocated and higher order)

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Two owners per channel

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Difficult to reason about typing & graph structure simultaneously

Contribution: connectivity graph proof method

This work: connectivity graphs

- Method for factoring out graph reasoning from reasoning about typing
- Mechanized in the Coq proof assistant
- Applied to prove deadlock freedom for feature-rich session-typed language
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Run-time configuration ρ

Threads: $\{T_1 \mapsto e_1, \ldots, T_6 \mapsto e_6\}$
Channels: $\{C_1 \mapsto \mathrm{buf}_1, \ldots, C_5 \mapsto \mathrm{buf}_5\}$

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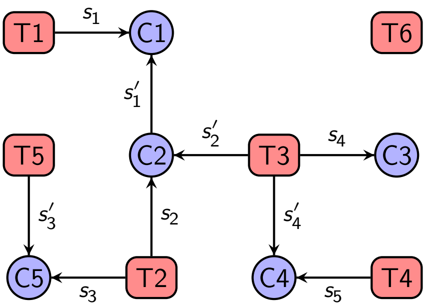
Connectivity graph G



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$G \vDash \rho$

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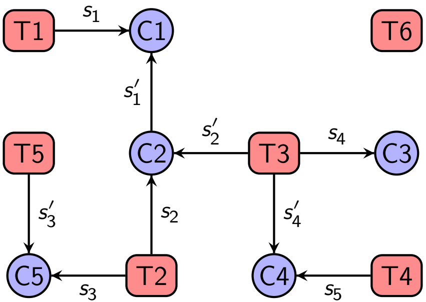

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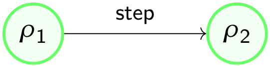

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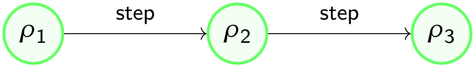

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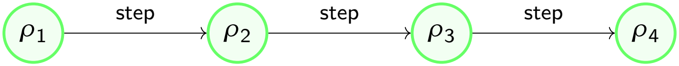

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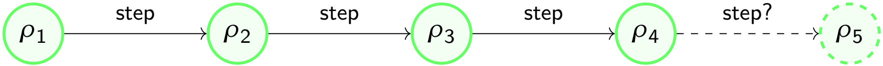

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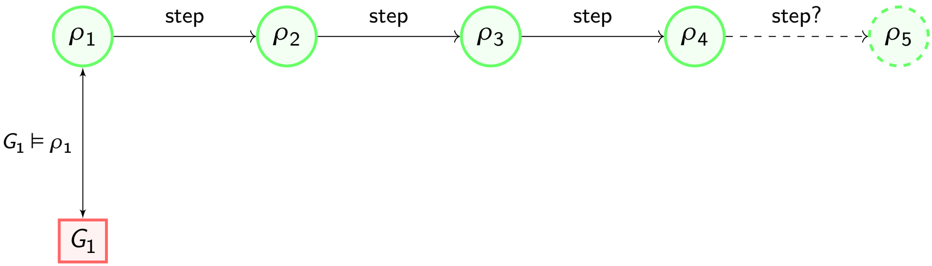

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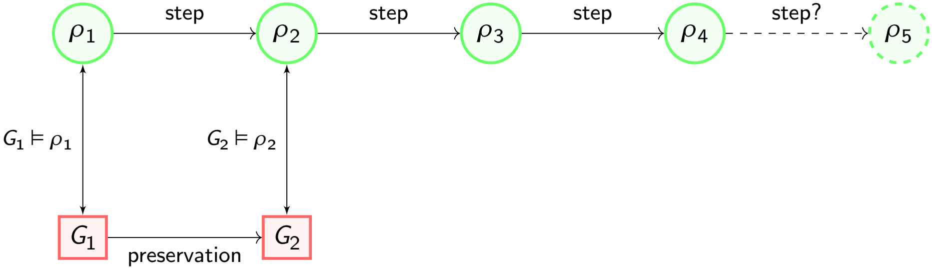

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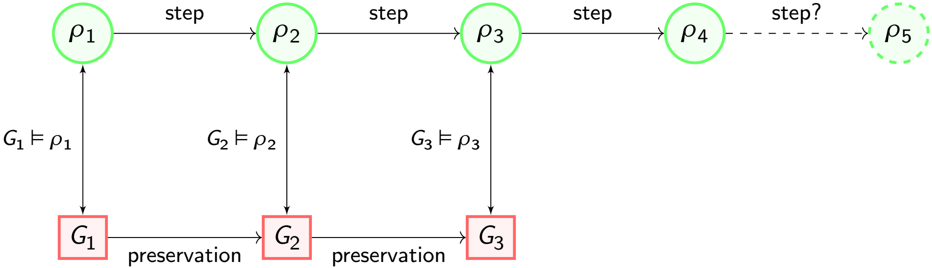

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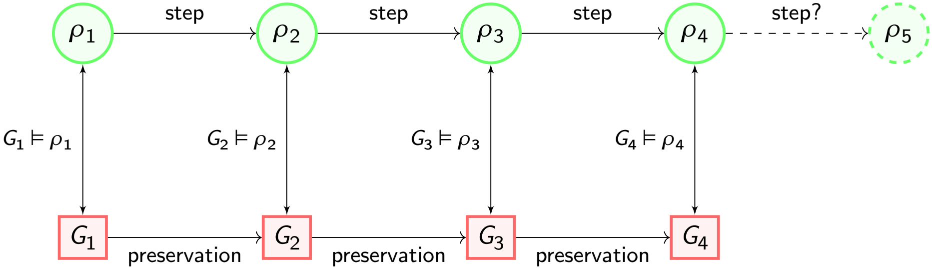

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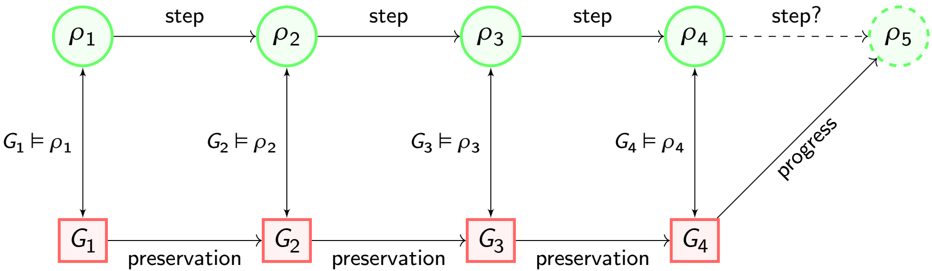

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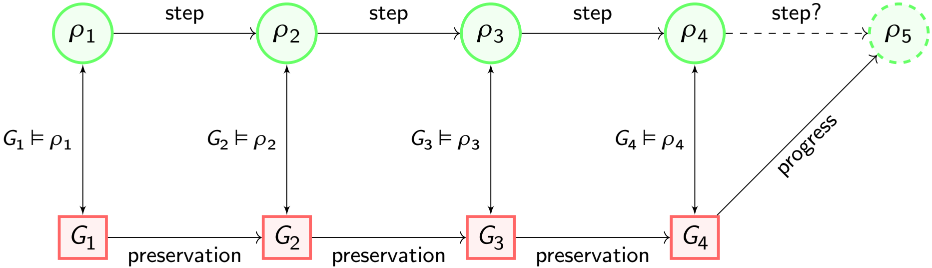

Connectivity graph framework:

- $\mathit{Cgraph}(V, L)$ data type for acyclic labeled graphs
- Generic construction for $G \vDash \rho$
  - Parameterized by local separation logic predicate $P_\rho(v)$ for each vertex $v \in G$
- Preservation: graph transformations in separation logic
- Progress: waiting induction principle for $\mathit{Cgraph}(V, L)$

All generic over vertices V and labels L

Linear heap typing in separation logic: (cf. Rouvoet [2020]'s definitional interpreters)

$$\frac{\Sigma_1 \vdash e_1 : \tau_1 \qquad \Sigma_2 \vdash e_2 : \tau_2 \qquad \Sigma_1 \cap \Sigma_2 = \emptyset}{\Sigma_1 \cup \Sigma_2 \vdash (e_1, e_2) : \tau_1 \times \tau_2}$$

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$$\frac{\Sigma = \{\mathsf{Chan}(a) \mapsto (t, s)\}}{\Sigma \vdash \#a_t : s} \quad \Rightarrow \quad \frac{\mathsf{own}(\mathsf{Chan}(a) \mapsto (t, s))}{\#a_t : s}*$$

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Lemmas in separation logic:

$$(\Sigma \vdash K[e] : B) \iff \exists A, \Sigma_1, \Sigma_2.\ (\Sigma_1 \cap \Sigma_2 = \emptyset) \wedge (\Sigma = \Sigma_1 \cup \Sigma_2) \wedge (\Sigma_1 \vdash e : A) \wedge$$
$$\forall e', \Sigma_3.\ (\Sigma_2 \cap \Sigma_3 = \emptyset) \wedge (\Sigma_2 \vdash e' : A) \rightarrow (\Sigma_2 \cup \Sigma_3 \vdash K[e'] : B)$$

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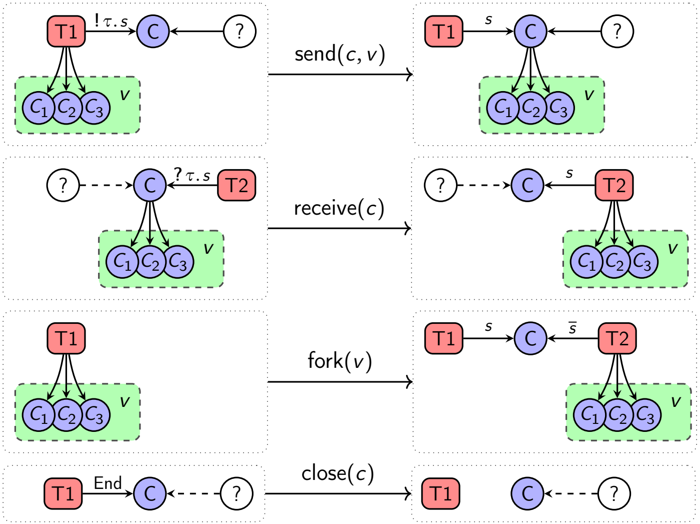
$$\Rightarrow$$

$$(K[e] : B) \quad \dashv\vdash \quad \exists A.\ (e : A) * \forall e'.\ (e' : A) -\!\!* (K[e'] : B)$$

We use the Iris proof mode to reason in separation logic (Krebbers et al. [2017])

Preservation via local graph transformations



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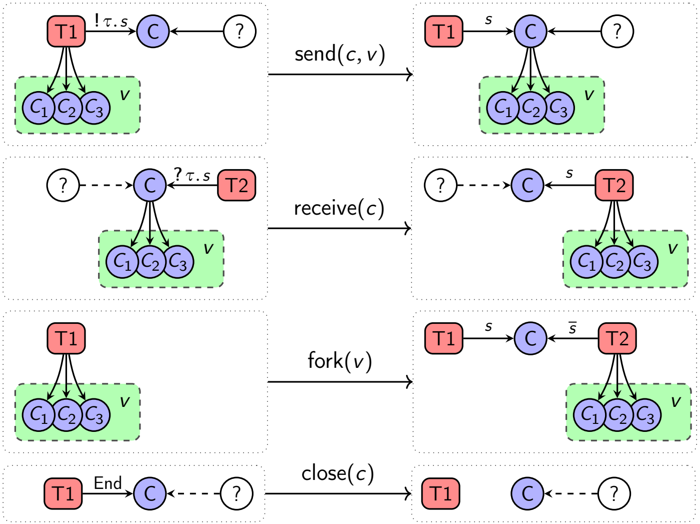

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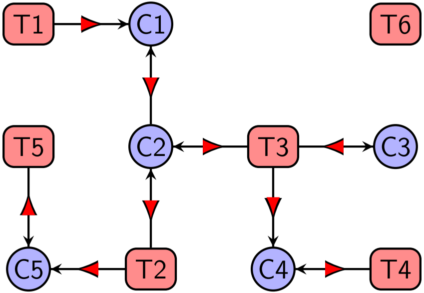
All in separation logic:

$$P_\rho(v_1) * (\text{own}(v_2 \mapsto \ell) \twoheadrightarrow P_\rho(v_2)) \vdash (\text{own}(v_2 \mapsto \ell') \twoheadrightarrow P_{\rho'}(v_1)) * P_{\rho'}(v_2)$$

Explained in our paper!

Progress via waiting induction

Connectivity graph with *waiting dependencies* (►) derived from run-time configuration



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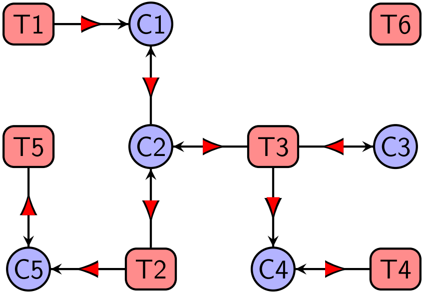

Lemma (Waiting induction)

Let $R(v, w)$ be any relation on the vertices. To prove $P(v)$, we may assume $P(w)$ for all w such that $v \longrightarrow w$ and $R(v, w)$, or $w \longrightarrow v$ and $\neg R(w, v)$

Our language

Functional language + session-typed channels (extension of Wadler [2012]'s GV)

Unrestricted and linear types

- Unrestricted: numbers, sums, products, unrestricted function type ($\rightarrow$)
- Linear: channels, sums, products, linear function type ($\multimap$)

General recursive types:

- Recursive session types, including through the message (example: $\mu X.\ !X.\mathit{End}$)
- Algebraic data types using recursion + sums + products
- Recursive types mechanized using coinduction (Gay et al. [2020])

Stronger deadlock and leak freedom result

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Our POPL reviewers: Can your method prove something stronger?

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Lemma. All threads and channels are reachable $\iff$ no partial deadlock
Lemma. Any thread or channel is reachable $\implies$ global progress
Theorem. For well-typed initial programs, no partial deadlock occurs

Mechanization

Mechanization in Coq:

- Generic *Cgraph*(V, L) library: 4999 LOC
- Language definition: 451 LOC
- Language specific deadlock and leak freedom proof: 1688 LOC

`https://github.com/julesjacobs/cgraphs`

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Questions?

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