

Deadlock-Free Separation Logic

Linearity Yields Progress for Dependent Higher-Order Message Passing

Jules Jacobs

Jonas Kastberg Hinrichsen

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Deadlock-Free Separation Logic: Linearity Yields Progress for Dependent Higher-Order Message Passing

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[JONAS KASTBERG HINRICHSEN](#), Aarhus University, Denmark

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We introduce a linear concurrent separation logic, called **LinearActris**, designed to guarantee deadlock and leak freedom for message-passing concurrency. LinearActris combines the strengths of session types and concurrent separation logic, allowing for the verification of challenging higher-order programs with mutable state through dependent protocols. The key challenge is to prove the adequacy theorem of LinearActris, which says that the logic indeed gives deadlock and leak freedom “for free” from linearity. We prove this theorem by defining a step-indexed model of separation logic, based on *connectivity graphs*. To demonstrate the expressive power of LinearActris, we prove soundness of a higher-order (GV-style) session type system using the technique of logical relations. All our results and examples have been mechanized in Coq.

CCS Concepts: • **Theory of computation** → **Separation logic**; *Program verification*; Programming logic.

Additional Key Words and Phrases: Message passing, deadlocks, session types, separation logic, Iris, Coq

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POPL'24

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POPL'24



The Iris Masterplan

Jules Jacobs, 2023-12-11

History

History

$\{P\} e \{Q\}$

History

$$\{P\} e \{Q\}$$

If P holds, and we run e , then Q holds

History

$$\{P\} e \{Q\}$$

~~If P holds, and we run e, then Q holds~~

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If P holds, then e is *safe*, and afterwards Q holds

History

$$\{P\} e \{Q\}$$

~~If P holds, and we run e, then Q holds~~

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Verification of programs with

mutable local variables

+ *pointers & data structures*

+ *shared-memory concurrency*

Using

Hoare logic (Hoare)

Separation logic (Reynolds)

Concurrent separation logic (O'Hearn)

What is Iris?

Iris from the ground up

A modular foundation for higher-order concurrent separation logic

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Abstract

Iris is a framework for higher-order concurrent separation logic, which has been implemented in the Coq proof assistant and deployed very effectively in a wide variety of verification projects. Iris was designed with the express goal of simplifying and consolidating the foundations of modern separation logics, but it has evolved over time, and the design and semantic foundations of Iris itself have yet to be fully written down and explained together properly in one place. Here, we attempt to fill this gap, presenting a reasonably complete picture of the latest version of Iris (version 3.1), from first principles and in one coherent narrative.

1 Introduction

Iris is a framework for higher-order concurrent separation logic, implemented in the Coq proof assistant, which we and a growing network of collaborators have been developing actively since 2014. It is the only verification tool proposed so far that supports

- foundational machine-checked proofs of
- deep correctness properties for

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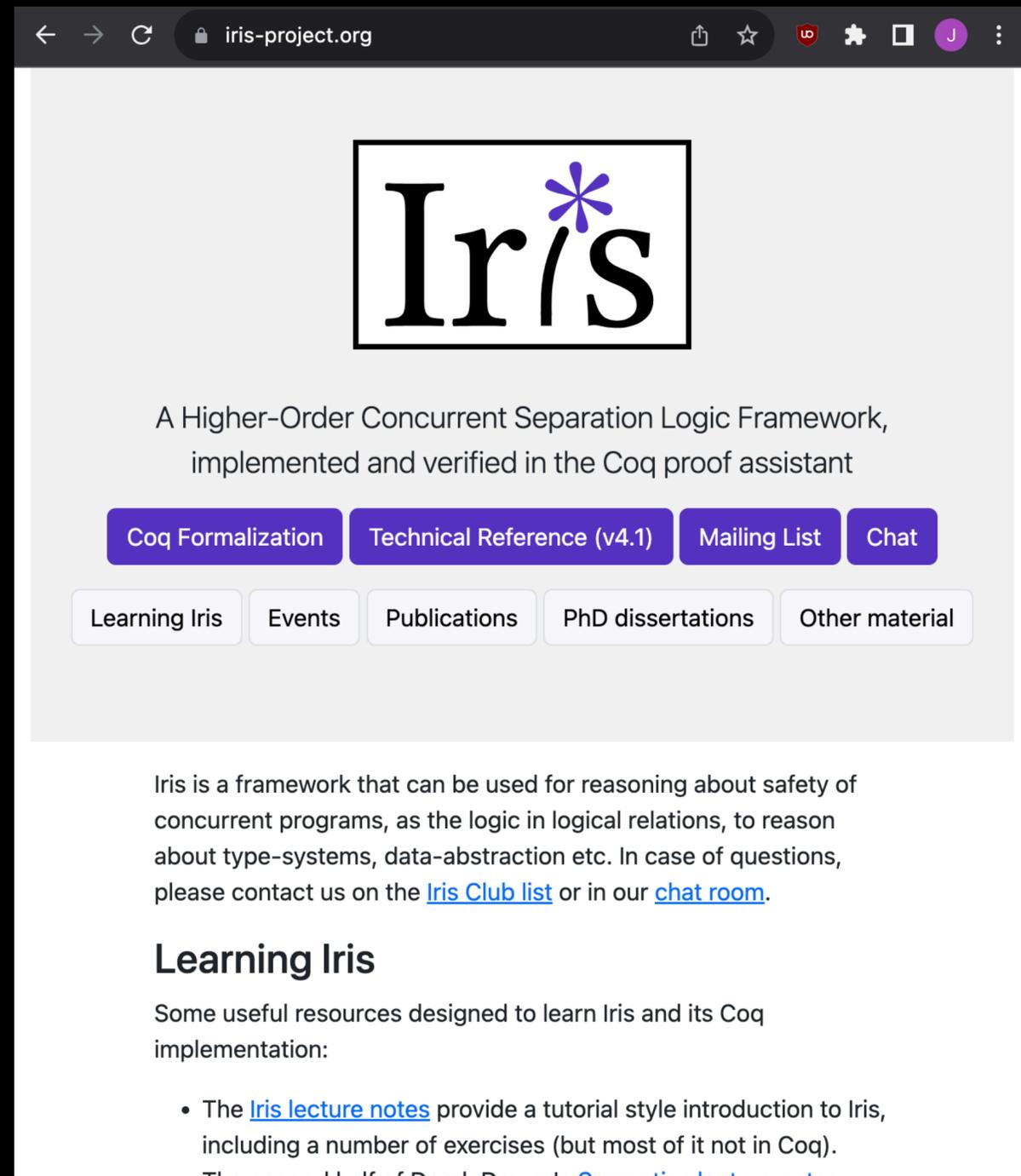
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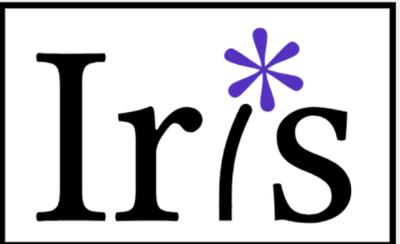
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iris-project.org



A Higher-Order Concurrent Separation Logic Framework,
implemented and verified in the Coq proof assistant

Coq Formalization Technical Reference (v4.1) Mailing List Chat

Learning Iris Events Publications PhD dissertations Other material

Iris is a framework that can be used for reasoning about safety of concurrent programs, as the logic in logical relations, to reason about type-systems, data-abstraction etc. In case of questions, please contact us on the [Iris Club list](#) or in our [chat room](#).

Learning Iris

Some useful resources designed to learn Iris and its Coq implementation:

- The [Iris lecture notes](#) provide a tutorial style introduction to Iris, including a number of exercises (but most of it not in Coq).
- The second half of Derek Dreyer's [Semantics lecture notes](#)

What is Iris, really?

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Coq

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Coq



What is Iris, really?

HeapLang $\{P\} e \{Q\}$

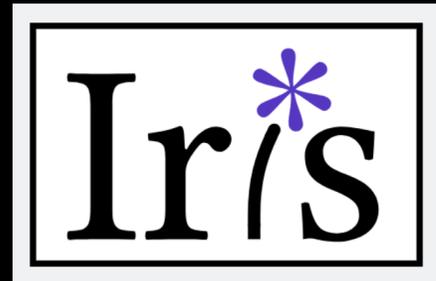
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Definition acquire : val := rec: "acquire" "l" :=
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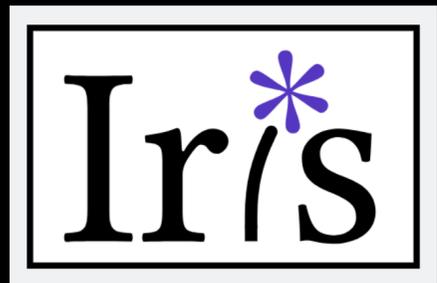
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iProp $\{P\} e \{Q\}$

Definition lock_inv (γ : gname) (l : loc) (R : iProp Σ) : iProp Σ :=
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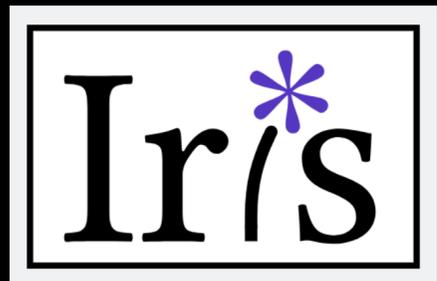
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Definition locked (γ : gname) : iProp Σ := own γ (Excl ()).

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Coq



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Hoare Triples $\{P\} e \{Q\}$

Lemma acquire_spec γ lk R :
 $\{\{\{ is_lock \ \gamma \ lk \ R \}\}\}$ acquire lk $\{\{\{ RET \ #(); \ locked \ \gamma \ * \ R \}\}\}$.

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Proof.

```
iIntros ( $\Phi$ ) "#HI H $\Phi$ ". iLöb as "IH". wp_rec.  
wp_apply (try_acquire_spec with "HI"). iIntros ([]).  
- iIntros "[HIked HR]". wp_if. iApply "H $\Phi$ "; auto with iFrame.  
- iIntros "_". wp_if. iApply ("IH" with "[H $\Phi$ ]"). auto.
```

Qed.

Proof Mode

What is Iris, really?



Coq



Iris has many innovations over CSL!

I will not talk about these

Ask me, and I'll tell you what I know

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Proof Mode

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$\text{safe}(e) :=$ “no illegal operations when running e ”

(based on operational semantics for HeapLang in Coq, with heap + thread pool)

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Partial correctness: e may loop!

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(based on operational semantics for HeapLang in Coq, with heap + thread pool)

Partial correctness: e may loop!

Very partial: e may deadlock!

Why are there 91 Iris papers?

Publications

Below, we give an overview of the research that uses Iris one way or another.

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[4] Trillium: Higher-Order Concurrent and Distributed Separation Logic for Intensional Refinement

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The Masterplan



The Masterplan



Build a fully verified software stack

Hypervisor, OS, compiler, type system, database,

web server, ..., and your code!

The Masterplan

All programming paradigms

Free theorems from types

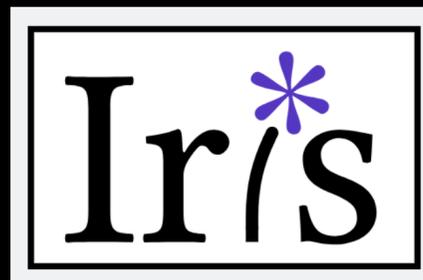
Strong adequacy theorems

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